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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Yasuo KOBAYASHI, et al.

U.S. Serial No.: 10/585,994

Group Art Unit: To Be Assigned

Filed: : July 13, 2006

Examiner: To Be Assigned

For:

OCT 11 2006

MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE

AND FILM DEPOSITION SYSTEM

THIRD INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. 1.56, Applicants are enclosing an Information Disclosure Citation Form (PTO-1449) and a copy of JP10-144667A, which is cited and discussed on pages 1 and 2 of Applicants' specification.

It is respectfully requested that the cited document be considered by the Examiner in the above-identified patent application and that the cited document be made officially of record therein. It is further requested that a listing of the same appear on the face of any patent which may issue from this application.

This Information Disclosure Statement is being filed in advance of the receipt of a first Office Action in this application. Therefore, it is believed that no fees are due under 37 C.F.R. Section 1.97(b)(3).

> Respectfully submitted, SMITH, GAMBRELL & RUSSELL, LLP

By:

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INFORMATION DISCLOSURE STATEMENT

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U.S. PATENT DOCUMENTS

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	DOCUMENT NUMBER	DATE	NAME	CLASS		FILING DATE, IF APPROPRIATE
AA	,,,,,,	-				
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FOREIGN PATENT DOCUMENTS

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Examiner's		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLA YES	TION NO
	AH	10-144667	5/29/98	Japan			Abstract	
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OTHER INFORMATION (Including Author, Title, Date, Pertinent Pages, Etc.)

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EXAMINER:

DATE CONSIDERED:

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.